

Submission data for 2023 CORE conference ranking process International Conference on Algorithms and Architectures for Parallel Processing

Yang Xiang

Introductory Questions

Conference

Title: International Conference on Algorithms and Architectures for Parallel Processing

Acronym: ICA3PP

Rank: B

Requested Rank

Rank: B

Conference Details

Month: October Publisher: Springer Bi-annual: False Multiconference: False

Component in a multi-conference or umbrella event: False

Colocated with other events: False

Alternative content: True

Alternative content description: Short papers

Proceedings Publishing Style

Proceedings Publishing: series

 $Link\ to\ most\ recent\ proceedings:\ \texttt{https://link.springer.com/book/10.1007/978-3-031-22677-9}$

Further details:

Most Recent Years

Most Recent Year

Year: 2022

URL: https://ica3pp2022.compute.dtu.dk Location: Copenhagen, Denmark (Online)

Papers submitted: 91 Papers published: 33 Acceptance rate: 36

Source for numbers: https://link.springer.com/book/10.1007/978-3-031-22677-9#toc

General Chairs

Name: Laurence T. Yang

Affiliation: St Francis Xavier University, Canada

Gender: Other H Index: 94

GScholar url: https://scholar.google.com/citations?hl=en&user=a39Yz5cAAAAJ

DBLP url:

Program Chairs

Name: Rongxing Lu

Affiliation: University of New Brunswick, Canada

Gender: M H Index: 85

GScholar url: https://scholar.google.com/citations?user=DeBXKOUAAAAJ&hl=en

DBLP url:

Second Most Recent Year

Year: 2021

URL: http://nsclab.org/ica3pp2021/

Location: Online Papers submitted: 403 Papers published: 145 Acceptance rate: 36

Source for numbers: https://link.springer.com/book/10.1007/978-3-030-95384-3

General Chairs

Name: Jean-Luc Gaudiot

Affiliation: University of California, Irvine

Gender: M H Index: 33

GScholar url: https://scholar.google.com/citations?hl=en&user=K4R30ooAAAAJ

DBLP url:

Program Chairs

Name: Aniello Castiglione

Affiliation: University of Salerno, Italy

Gender: M H Index: 43

 $GScholar\ url:\ \texttt{https://scholar.google.com/citations?hl=en\&user=OfHbNZEAAAAJ}$

DBLP url:

Third Most Recent Year

Year: 2020

URL: http://www.cloud-conf.net/ica3pp2020/

Location: New York, USA Papers submitted: 495 Papers published: 147 Acceptance rate: 30

Source for numbers: https://link.springer.com/content/pdf/bfm:978-3-030-60245-1/1?pdf=chapter%20toc

General Chairs

Name: Meikang Qiu

Affiliation: Columbia University, USA

Gender: Other H Index: 99

GScholar url: https://scholar.google.com/citations?hl=en&user=smMVdtwAAAAJ

DBLP url:

Program Chairs

Name: Bhavani Thuraisingham

Affiliation: University of Texas at Dallas, USA

Gender: F H Index: 68

GScholar url: https://scholar.google.com/citations?hl=en&user=o_xUNWkAAAAJ

DBLP url:

Policies

Chair Selection: Processes and Criteria for Selecting Chairs of ICA3PP Updated: 08/09/2020 ICA3PP (the International Conference on Algorithms and Architectures for Parallel Processing) is an important event in the field of parallel and distributed computing. The General Co-Chairs (GC) and Program Chairs (PC) are selected by the Steering Committee Chairs. They are nominated based on their world-leading research as evidenced by their influence in service computing and related areas and their experiences in organizing

cognate international conferences. They decide autonomously on the distribution of responsibilities within each group. The GC and PC propose qualified individuals for the other roles in the Organising Committee (e.g., Publicity Chair, Web Chair, Registration Chair and Publication Chair), typically 1 to 4 persons per role, taking into account gender and geographical representation (except for the local co-chairs). The GC and PC chair teams typically represent different continents, such as Europe and Africa, the Americas, Asia, Australia, and the Pacific. The names are finally approved by the Steering Committee Chairs. The detailed procedure and criteria for selecting conference chairs of ICA3PP can be outlined as follows: 1. Call for Proposals: The Steering Committee Chairs send out a call for proposals to host and chair the conference. Interested parties submit proposals that include their qualifications, potential venue, budget, and other relevant details. 2. Review of Proposals: The Steering Committee Chairs review the proposals. The committee may comprise previous conference chairs, committee members, and influential members from the relevant academic or professional community. 3. Criteria for Selection: The selection is generally based on several factors, such as: • Experience: The candidate should have prior experience in organizing conferences or similar events. • Academic/Professional Standing: The candidate should have a strong academic or professional reputation in the field of parallel processing. They should have a proven track record of research and/or contributions to the field. • Organizational Capacity: The candidate should demonstrate the capacity to organize a large-scale event, including managing logistics, finances, and personnel. • Geographical Diversity: Often, the conference rotates among different geographical areas to promote diversity and inclusion. • Venue: The proposed venue should be suitable for the conference in terms of size, location, accessibility, and facilities. • Budget: The proposed budget should be realistic and should demonstrate that the conference can be hosted successfully without financial difficulties. 4. Interviews: The committee may conduct interviews or discussions with potential candidates to gain more insight into their proposals. 5. Selection: After a thorough review, the committee selects the conference chairs based on the criteria mentioned above. The decision is usually made by a majority vote. 6. Announcement: Once the chairs are selected, an official announcement is made, and the selected chairs begin the process of organizing the conference. 7. Mentorship: Often, new chairs are mentored by previous conference chairs to ensure a smooth transition and successful organization of the conference each year. Policy name: Chair Selection Criteria

Policy url: http://nsclab.org/ica3pp/ICA3PP_selection.pdf

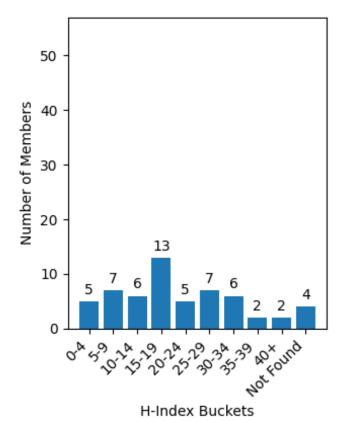
Program Committee

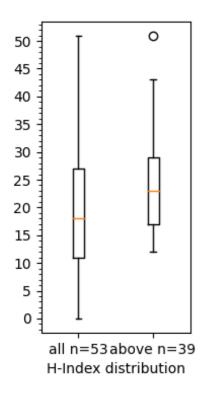
Link to pc: https://ica3pp2022.compute.dtu.dk/committees.html

File: http://portal.core.edu.au/core/media/2023/pc_members/ICA3PP22_PC_H0xeHTC.txt

H-index plot: http://portal.core.edu.au/core/media/2023/pc_graphs/higherrank_hindex_buckets_2214.png

Information contained within these graphs is derived using the Elsevier Scopus Database 2023. Scopus h-index is generally about 30% lower than Google Scholar h-index.





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http://portal.core.edu.au/core/media/2023/conf_submissions_clean_spc/higherrank2214_spc_report.csv WPP Report: http://portal.core.edu.au/core/media/2023/wpp_reports/p5XCqUKY.txt
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1. International Conference on Algorithms and Architectures for Parallel Processing (ICA3PP) Core Rank: B

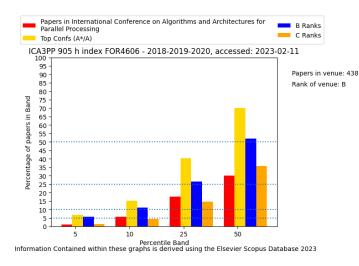
This venue was published at 10 times by 7 of 31 individuals in the last 5+ years.

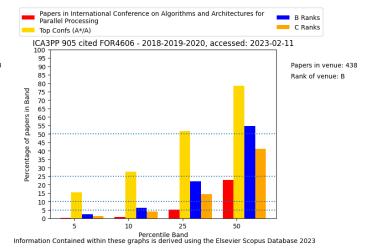
The individuals that publish at this venue are: Qianhong Wu(3), Ladjel Bellatreche(2), Fagen Li(1), Junlong Zhou(1), Laurent Lefevre(1), Mingwu Zhang(1), Shigeng Zhang(1)

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In 2018, there were 3 publications by 3 individuals: Fagen Li, Ladjel Bellatreche, Laurent Lefevre In 2019, there were 1 publications by 1 individuals: Ladjel Bellatreche
In 2020, there were 2 publications by 1 individuals: Qianhong Wu
In 2021, there were 2 publications by 2 individuals: Mingwu Zhang, Shigeng Zhang
In 2022, there were 2 publications by 2 individuals: Junlong Zhou, Qianhong Wu
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7 out of the 31 individuals published at this venue in 1 or more years 2 out of the 31 individuals published at this venue in 2 or more years

Centile graphs of paper metrics





Top People Involvement

Not Providing Involvement

Additional Data

Google Scholar Data

 $Sub-category\ url:\ https://scholar.google.com/citations?hl=en\&view_op=search_venues\&vq=ICA3PP\&btnG=lineships.$

Position in sub-category: 20+ h5 index of 20th item in category: 42 h5 index for this conference: 13

Relationship to similar conferences

Partial ordering of similar conferences in the area, with argument as to where the current venue fits and why:

1: International Conference on Algorithms and Architectures for Parallel Processing (ICA3PP) (B) 2: IEEE International Conference on High Performance Computing and Communications (HPCC) (B) 3: International Symposium on Parallel and Distributed Processing with Applications (ISPA) (C) 4: International Conference on Trust, Security and Privacy in Computing and Communications (TrustCom) (B) 5: International Conference on Parallel Processing (ICPP) (A) 6: International Conference on Parallel and Distributed Systems (ICPADS) (B) 7: IEEE International Parallel and Distributed Processing Symposium (IPDPS) 8: IEEE/ACM International Symposium on Cluster, Cloud and Internet Computing (CCGRID) (A) 9: IFIP International Conference on Network and Parallel Computing (NPC) (C)

Other Information

Attachments

Proposers

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Submitted By

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